

# Final Practical Exam /viva

Subject: Micro Processor and Computer Architecture

Time 30 Min.

BCA Second Semester

F.M 20

## SET A.

- a) Explain Adder circuit diagram
- b) Draw 8259 block diagram
- c) Explain Addition and Subtraction with example

## SET B.

- a) Explain the opcode fetch machine cycle for MVI A,32H with timing diagram.
- b) Draw 8085 block diagram
- c) Explain Subtractor circuit diagram

## SET C.

- a) Show the step by step multiplication process using Booth's Algorithm, when +14 is multiplied by -14. Assume 5 bit registers that hold signed numbers.
- b) Explain Parallel data transfer in detail.
- c) Explain Keyboard display controller