TribhuvanUniversity

Faculty of Humanities & Social Sciences
OFFICE OF THE DEAN
2018

Bachelor in Computer Applications   **Full Marks: 60**
Course Title: Microprocessor and Computer Architecture   **Pass Marks: 24**
Code No: CACS 155   **Time: 3 hours**
Semester: II

Candidates are required to answer the questions in their own words as far as possible.

Group - B

Attempt any SIX questions.   **[6 x 5 = 30]**

1.   Explain the Bus organization of 8085 Microprocessor.
2.   Explain the opcode fetch machine cycle for MVI A, 32H with timing diagram.
3.   Explain the 8085 Instruction addressing modes with example.
4.   Explain the memory hierarchy with diagram.
5.   Explain the organization of Microprogrammed Control Unit.
6.   Define control word. Explain the procedure for generating control word for specific operation.
7.   Define instruction pipeline. Explain the four-segment instruction pipeline with example.

Group - C

Attempt any Two Questions.   **[10 x 2 = 20]**

8.   Explain the functional block diagram of 8085 Microprocessor.
9.   Explain the design and control logic of Accumulator.
10.   Explain the different data transfer and manipulation instructions with example.

Best of Luck!